# NMS2S-18-1K1

DC~18GHz, SPDT, PULSE LATCING

Features:
* Low VSWR
* Low Insertion Loss
* High Isolation

Applications:
* Wireless
* Transmitter
* Laboratory Test
* Radar

## Electrical

| | |
|---|---|
| Frequency: | DC~18GHz |
| Impedance: | 50Ω |
| Operation Mode: | Pulse Latching |
| Switch Sequence: | Break before make |
| Current: | 100mA |
| Voltage: | +24V |

| Frequency range (GHz) | Insertion Loss (dB) | Isolation (dB) | VSWR | POWER |
|---|---|---|---|---|
| DC-6 | 0.2 | 70 | 1.2 | 80 |
| 6-12 | 0.25 | 70 | 1.3 | 60 |
| 12-18 | 0.4 | 60 | 1.4 | 50 |

## Mechanical

| | |
|---|---|
| Size*2: | See Drawing |
| Switching Sequence: | Break before Make |
| Switching Time: | 15mS max. |
| Operation Life: | 2M Cycles |
| Vibration (operating): | 20-2000Hz, 10G RMS |
| Mechanical Shock (non-operating): | 50G, 1/2Sine, 12msec |
| RF Connectors: | SMA Female |
| Power Supply & Control Interface Connectors: | D-SUB 9 Pin Male |
| Mounting: | 3-Φ3.8mm through-hole |

[2] Exclude connectors.

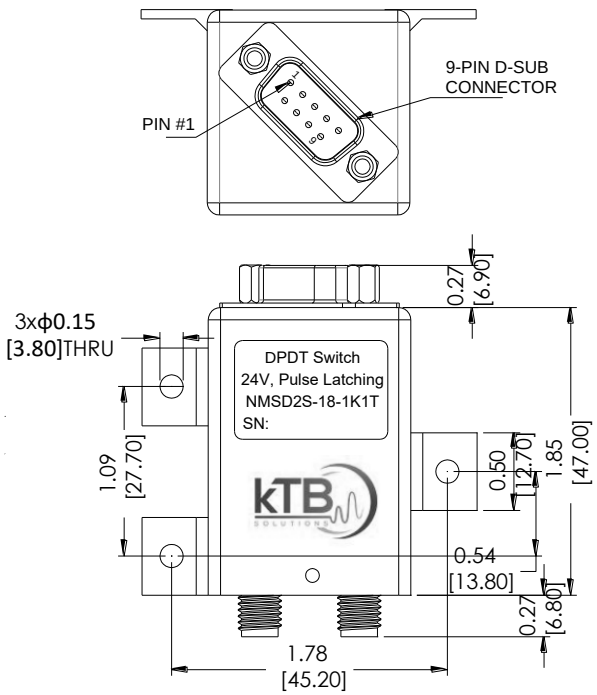
## Environmental

| | |
|---|---|
| Temperature: | -25~+65℃ |
| Extended Temperature: | -45~+85℃ |

## Latching&TTL

| Pin | Function | DPDT |
|---|---|---|
| 1 | V1 | POS1:J1-J3,J2-J4 |
| 2 | V2 | POS1:J1-J2,J3-J4 |
| 3 | -COM | |
| 4~9 | NC | |

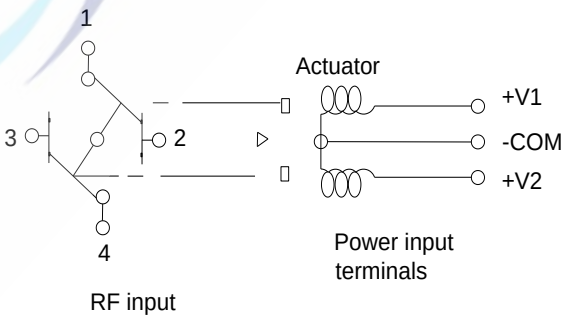
## Outline Drawings




Unit: mm [in]

Tolerance: ±0.5mm [±0.02in]

## Driving schematic diagram



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**Typical Performance Data:**